

ABSTRACT OF THE DISCLOSURE

In a tamper resistant microprocessor having a cache memory, the cache memory stores the decrypted
5 execution code or data into one of cache lines provided in the cache memory, each cache line having a secret protection attribute holding section for storing an actual encryption key used in decrypting the execution code or data, and a cache memory control unit processes
10 a reading request for the execution code or data such that, if the execution code or data exists in the cache memory and the execution code or data in the cache memory is decrypted by an identical encryption key as the prescribed encryption key, the execution code or
15 data in the cache memory is read out.

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